## Features

- Low Insertion Loss 0.3 dB at 1 GHz
- High Isolation 26 dB at 1 GHz
- Low Control Voltage 2.6V to 5.0V
- Harmonics H2:
-80dBc@1GHz
- GaAs pHEMT Process


## Applications

- Cellular Handset Applications
- Antenna Tuning Applications
- Multi-Mode GSM, W-CDMA Applications
- IEEE802.11b/g WLAN Applications
- GSM/GPRS/EDGE Switch Applications
- Cellular Infrastructure Applications


> Functional Block Diagram

## Product Description

The RF1200 is a single-pole double-throw (SPDT) high power switch specially designed to handle GSM power applications. The RF1200 features low insertion loss, low control voltage, high linearity, and very good harmonic characteristics. It is fabricated with $0.5 \mu \mathrm{~m}$ GaAs pHEMT process, and is packaged in a very compact $2 \mathrm{~mm} \times 2 \mathrm{~mm}, 6$-pin, leadless QFN package.

## Ordering Information

| RF1200 | SPDT Switch |
| :--- | :--- |
| RF1200PCBA-410 | Fully Assembled Evaluation Board |

Optimum Technology Matching ${ }^{\circledR}$ Applied

| $\square$ GaAs HBT | $\square$ SiGe BiCMOS | $\square$ GaAs pHEMT | $\square$ GaN HEMT |
| :--- | :--- | :--- | :--- |
| $\square$ GaAs MESFET | $\square$ Si BiCMOS | $\square$ Si CMOS |  |
| $\square$ InGaP HBT | $\square$ SiGe HBT | $\square$ Si BJT |  |

## Absolute Maximum Ratings

| Parameter | Rating | Unit |
| :--- | :---: | :---: |
| Voltage | 7.0 | V |
| Maximum Input Power $(0 \mathrm{GHz}$ to <br> $2.5 \mathrm{GHz})$ | +36 | dBm |
| Operating Temperature | -30 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | -35 to +100 | ${ }^{\circ} \mathrm{C}$ |

4 Caution! ESD sensitive device.
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| Parameter | Specification |  |  | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min. | Typ. | Max. |  |  |
|  |  |  |  |  | Temp $=25^{\circ} \mathrm{C}, \mathrm{V}_{\text {CONTROL }}=2.65 \mathrm{~V}$ |
| Insertion Loss |  |  |  |  |  |
| RF>ANT |  | 0.3 | 0.4 | dB | RF ON, 0.88GHz |
| RF>ANT |  | 0.4 | 0.5 | dB | RF ON, 1.88GHz |
| RF>ANT |  | 0.5 | 0.6 | dB | RF ON, 2.10GHz |
| RF>ANT |  | 0.55 | 0.65 | dB | RF ON, 2.45GHz |
| RF>ANT Isolation |  |  |  |  |  |
| RF>ANT | 25 | 26 |  | dB | RF ON, 0.88GHz |
| RF $>$ ANT | 21 | 22 |  | dB | RF ON, 1.88GHz |
| RF $>$ ANT | 19 | 20 |  | dB | RF ON, 2.10GHz |
| RF>ANT | 17 | 18 |  | dB | RF ON, 2.45GHz |
| 0.8 GHz to 1 GHz Harmonics |  |  |  |  |  |
| Second Harmonic |  | -80 |  | dBc | $\mathrm{P}_{\mathrm{IN}}=34.5 \mathrm{dBm}, 0.88 \mathrm{GHz}, 2 \mathrm{f}_{0}$ |
| Third Harmonic |  | -75 |  | dBc | $\mathrm{P}_{\mathrm{IN}}=34.5 \mathrm{dBm}, 0.88 \mathrm{GHz}, 3 \mathrm{f}_{0}$ |
| 1.7 GHz to 2.0 GHz Harmonics |  |  |  |  |  |
| Second Harmonic |  | -80 |  | dBc | $\mathrm{P}_{\mathrm{IN}}=31.5 \mathrm{dBm}, 1.9 \mathrm{GHz}, 2 \mathrm{f}_{0}$ |
| Third Harmonic |  | -80 |  | dBc | $\mathrm{P}_{\mathrm{IN}}=31.5 \mathrm{dBm}, 1.9 \mathrm{GHz}, 3 \mathrm{f}_{0}$ |
| 2.45GHz Harmonics |  |  |  |  |  |
| Second Harmonic |  | -90 |  | dBc | $\mathrm{P}_{\mathrm{IN}}=31.5 \mathrm{dBm}, 1.9 \mathrm{GHz}, 2 \mathrm{f}_{0}$ |
| Third Harmonic |  | -90 |  | dBc | $\mathrm{P}_{\mathrm{IN}}=31.5 \mathrm{dBm}, 1.9 \mathrm{GHz}, 3 \mathrm{f}_{0}$ |
| IMD Due to Out-of-Band Blocker |  |  |  |  |  |
| RF>ANT |  | -105 |  | dBm | $\begin{aligned} & \mathrm{P}_{\mathrm{IN}}=20 \mathrm{dBm} @ 1950 \mathrm{MHz}, \mathrm{P}_{\mathrm{BLOCK}}=-15 \mathrm{dBm} @ \\ & 4090 \mathrm{MHz} \end{aligned}$ |
| RF Port Return Loss |  |  |  |  |  |
| RF>ANT |  | 15 |  | dB | 0.5 GHz to 2.5 GHz |
| Input Power at 0.1 dB Compression Point |  |  |  |  |  |
|  | 37 |  |  | dBm | 0.88GHz |
|  | 34 |  |  | dBm | 1.88 GHz |
| Switching Speed |  |  |  |  |  |
|  |  |  | 5 | us |  |

Note: Parameters hold at $25^{\circ} \mathrm{C}$ and $\mathrm{V}_{\text {CONTROL }}=2.65 \mathrm{~V}$.

## Switch Control Settings

|  | Control Signals |  | Signal Paths |  |
| :---: | :---: | :---: | :---: | :---: |
|  | VRF1 | VRF2 | RF1-RFC | RF2-RFC |
| Valid States | 1 | 0 | Closed | Open |
|  | 0 | 1 | Open | Closed |
| Invalid | 0 | 0 | Indeterminate State* |  |
| States | 1 | 1 | Indeterminate State* |  |
|  |  |  |  |  |

0: Logic level low, OV~0.2V
1: Logic level high, 2.6V~5.0V
Note: In indeterminate states, both sigal paths are closed with degraded performance.

| Pin | Function | Description | Interface Schematic |
| :---: | :---: | :--- | :--- |
| $\mathbf{1}$ | RF1 | First RF connection. |  |
| $\mathbf{2}$ | GND | Ground. |  |
| $\mathbf{3}$ | RF2 | Second RF connection. |  |
| $\mathbf{4}$ | VRF2 | Second RF control. |  |
| $\mathbf{5}$ | RFC | Common RF connection. |  |
| $\mathbf{6}$ | VRF1 | First RF control. |  |
| Pkg <br> Base | GND |  |  |

## Package Drawing



## Evaluation Board Schematic


*L1 is optional for
IEC61000-4-2 ESD protection.

## Typical Performance

Temp $=25^{\circ} \mathrm{C}, \mathrm{V}_{\text {CONTROL }}=2.65 \mathrm{~V}$


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